



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. . . . . 09/234,233  
Filing Date . . . . . January 20, 1999  
Inventor . . . . . Weimin Li, et al  
Assignee . . . . . Micron Technology, Inc.  
Group Art Unit . . . . . 2818  
Examiner . . . . . D. Vu  
Attorney's Docket No. . . . . MI22-1035  
Title: Semiconductor Processing Methods

**INFORMATION DISCLOSURE STATEMENT**

References – See Attached Form PTO-1449


In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed together with the filing of the Request for Continued Examination (RCE) Application. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 03 Sep 2003

By:   
James E. Lake  
Reg. No. 44,854

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1035	SERIAL NO. 09/234,233	
<div style="position: relative; height: 100px;"> <div style="position: absolute; top: 0; left: 0; width: 100%; height: 100%; border: 2px solid black; border-radius: 50%; text-align: center; line-height: 100px; font-size: 24px; font-weight: bold;"> PIPE SEP 03 2003 PATENT &amp; TRADEMARK OFFICE </div> </div> <p style="margin-top: 10px;"><b>LIST OF ART CITED BY APPLICANT</b> (Use several sheets if necessary)</p>				APPLICANT Weimin Li et al		
				FILING DATE January 20, 1999		GROUP 2818
<b>U.S. PATENT DOCUMENTS</b>						
Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA 4,775,478	07/1988	Abernathy et al			
	AB 4,870,470	09/1989	Bass et al			
	AC 5,312,768	05/1994	Gonzalez			
	AD 5,439,838	08/1995	Yang			
	AE 6,001,747	12/1999	Annapragada			
	AF 6,518,122	02/2003	Chan			
	AG 6,492,688	12/2002	Ilg			
	AH 6,373,114	04/2002	Jeng			
	AI 2001/0019868	09/2001	Gonzalez et al			
	AJ					
	AK					
	AL					
<b>FOREIGN PATENT DOCUMENTS</b>						
	Document Number	Date	Country	Class	Subclass	Translation
						Yes No
	AM					
	AN					
	AO					
	AP					
	AQ					
<b>OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)</b>						
	AR		Wolf, S., "Silicon Processing for the VLSI Era," Vol. 1, pps. 189-190.			
	AS					
	AT					
EXAMINER			DATE CONSIDERED			
<p><small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small></p>						